

JC474 U.S. PRO  
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# TRANSMITTAL FORM

Electronic Version 1.0.3

Stylesheet Version: 1.0

Attorney Docket  
Number:

MTKP0019USA

Submission Type: Utility  
Patent Filing

## PHASE-LOCKED LOOP HAVING PHASE DETECTOR ERROR SIGNAL RESHAPING AND METHOD THEREOF

First Named Inventor: Hsu-Feng Ho

### SUBMITTED BY

Name:	Mr. Winston Hsu
Registration Number:	41,526
Electronic Signature Mark: Winston Hsu	Date Signed: 20020913
Name:	Mr. Mord Michael Lewis
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Electronic Signature Mark: Mord Michael Lewis	Date Signed: 20020913

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**Attached Files:**

declaration	MTKP0019DEC1.TIF
declaration	MTKP0019DEC2.TIF
specification	MTKP0019.xml
bibd-transmittal	MTKP0019apds.xml
patent-assignments	MTKP0019asgn.xml
fee-transmittal	MTKP0019fee.xml

**Attached Image File(s):**

MTKP0019DEC1.TIF  
MTKP0019DEC2.TIF



**Comments:**

# FEE TRANSMITTAL

Electronic Version 1.1.0

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*Patent fees are subject to annual revisions on or about October 1st of each year.*

Small Entity

Small Business Concern

## TOTAL FEES AUTHORIZED: \$ 410

**The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:**

Deposit Account Number: 500801



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Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

### SUBMITTED BY

Authorized Name: WINSTON HSU

Electronic Signature Mark: VAEB-JMXX-8IIL

Date Signed: 20020913

### BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	201	\$ 370

Subtotal For Basic Filing Fee: \$ 370

### EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 20	203	\$ 9	0	\$ 0
Independent Claims: 3	202	\$ 42	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0

## ADDITIONAL FEES

Fee Description	Number	Quantity	Fee Code	Amount	Fee Paid
Recording Each Patent Assignment Per Property Fee	00000000	1	581	\$ 40	\$ 40

Subtotal For Additional Fees: \$ 40

Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 17864  
Application ID: 10065045  
Title of Invention: PHASE-LOCKED LOOP HAVING  
PHASE DETECTOR ERROR SIGNAL  
RESHAPING AND METHOD  
THEREOF  
First Named Inventor: Hsu-Feng Ho  
Domestic/Foreign Application: Domestic Application  
Filing Date: null  
Effective Receipt Date: 2002-09-13   
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